

In re Patent Application of
PIO
Serial No. **Not Yet Assigned**
Filed: **Herewith**

23. A method according to Claim 22 wherein the enabling transistor is a MOS transistor.

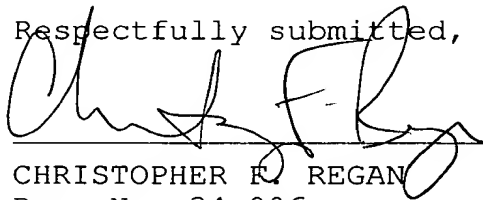
24. A method according to Claim 18 wherein the common control gate region overlies the common source region.

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25. A method according to Claim 18 wherein the memory cells of each pair of memory cells are arranged symmetrically with respect to the common source region.

REMARKS

It is believed that all of the claims are patentable over the prior art. Accordingly, after the Examiner completes a thorough examination and finds the claims patentable, a Notice of Allowance is respectfully requested in due course. Should the Examiner determine any minor informalities that need to be addressed, he is encouraged to contact the undersigned attorney at the telephone number below.

Respectfully submitted,



CHRISTOPHER F. REGAN
Reg. No. 34,906
Allen, Dyer, Doppelt, Milbrath
& Gilchrist, P.A.
255 S. Orange Avenue, Suite 1401
Post Office Box 3791
Orlando, Florida 32802
407-841-2330
407-841-2343 fax
Attorney for Applicant